

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) An electronic microcircuit module tape including a substrate, at least one contact area on a first face of this substrate, a second face of this substrate being capable of accommodating supporting an integrated circuit and being provided with cutouts exhibiting contact pad areas, wherein the tape further includes [[,]] a first adhesive means to retain a first face of a mask in position against the second face of the substrate, and wherein the mask is perforated to form comprises a cutout forming a window around adapted to subsequently receive the integrated circuit.
2. (Previously presented) The electronic microcircuit module tape according to claim 1, wherein the mask is made from a material identical to that of a card body provided to receive the module, from polyester or polyvinyl chloride.
3. (Previously presented) The electronic microcircuit module tape according to claim 1, wherein the mask has a thickness, defined with regard to the second face of the substrate on which it is mounted, greater than the height of the integrated circuit.
4. (Previously presented) The electronic microcircuit module tape according to claim 1, wherein the first adhesive means enables the integrated circuit to be retained on the substrate.

5. (Currently amended) A method for conditioning an electronic microcircuit module, characterised in that it includes the following stages consisting of

creating a contact area on a first face of a substrate tape,

arranging a first adhesive means between a second face of the substrate tape and a first face of a mask tape, to keep the mask tape in position against the second face,

perforating the mask tape so that a mask window is facing the contact area, and

arranging a second adhesive means on the second face of the mask tape.

6. (Currently amended) The method according to claim 5, characterised in that it includes an additional stage consisting of:

choosing a the mask tape in a material identical to that of a card on which the module is to be mounted.

7. (Currently amended) The method according to claim 5, characterised in that the mask tape has the form of a tape including several windows which are laminated on a support including several contact area before separation into individual units.

8. (Currently amended) The method according to claim 5, characterised in that the stage consisting in retaining the

mask tape in position against the second face of the substrate tape includes an operation consisting of:

laminating the first adhesive means on this second face of the substrate tape.

9. (Currently amended) The method according to claim 5, characterised in that the stage consisting in arranging ~~an~~ the first adhesive means on the mask tape includes an operation consisting of

depositing the adhesive means on the mask tape, and then perforating this mask tape before laminating it against the second face of the substrate tape.

10. (Currently amended) The method according to claim 5, characterised in that it includes a subsequent stage consisting in gluing an integrated circuit to the second face of the substrate tape, on the first adhesive means.

11. (Currently amended) The method according to claim 5, characterised in that it includes a stage consisting of:

gluing the mask tape equipped with an electronic circuit to the bottom of a card recess.

12. (Currently amended) The method according to claim 11, characterised in that the stage consisting in gluing the mask tape to the recess includes an operation consisting of:

depositing cyanoacrylate glue between the mask tape and the bottom of the recess.

13. (Currently amended) The method according to claim 11, characterised in that the stage consisting in gluing the mask tape into the recess includes an operation consisting of:

soldering by emission of ultrasound waves.

14. (Currently amended) The method according to claim 5,1, further comprising separating the individual module in the form of a parallelepiped.

15. (Currently amended) The electronic microcircuit module tape of claim 1,1, further comprising a second adhesive means dispensed on a second face of the mask.

16. (New) An electronic microcircuit module tape comprising:

a substrate tape having a first face and an opposite second face, wherein the second face is adapted to support a plurality of integrated circuits thereon;

a plurality of electrically conductive contact areas on the first face of the substrate tape; and

a mask tape having a first side attached to the second face of the substrate tape, wherein the mask tape comprises cutouts forming windows adapted to subsequently receive the integrated circuits when the integrated circuits are attached to the second face of the substrate tape.

17. (New) An electronic microcircuit module tape as in claim 16 wherein the substrate tape comprises cutouts to allow

access to the contact areas through the substrate tape from the windows.

18. (New) An electronic microcircuit module tape as in claim 16 wherein the substrate tape comprises a first adhesive layer to retain a first face of the mask film in position against the second face of the substrate film.

19. (New) An electronic microcircuit module tape as in claim 18 further comprising a second adhesive layer on a second opposite side of the mask tape.

20. (New) An electronic microcircuit module tape as in claim 16 wherein the substrate tape and the mask tape are adapted to be cut into individual modules after the integrated circuits are attached to the second face of the substrate tape.

21. (New) An electronic microcircuit module tape comprising:

a substrate tape having a first face and an opposite second face, wherein the second face is adapted to support an integrated circuit thereon; and

electrically conductive contact areas attached to the first face of the substrate tape,

wherein the substrate tape comprise apertures through the substrate tape to allow access to the contact areas on the first face of the substrate tape from the second face of the substrate tape by conductors subsequently connected between the integrated circuit and the contact areas when the integrated circuit is attached to the second face of the substrate tape, wherein the second

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face of the substrate tape is adapted to have a mask attached thereto which has a window to position the mask relative to the integrated circuit and conductors.